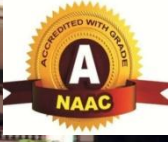




Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE and
Recognised by Govt. of Maharashtra)



Department of Electronics & Telecommunication Engineering In Association with Institution's Innovation Council (IIC) & Internal Quality Assurance Cell (IQAC)

Organizing workshop on

How to Write Technical Paper

Resource Person:

- **Dr. Machhindranath Devidas Patil**
Associate Professor,
Dept. of Automation & Robotics, VESIT
Head, VESIT Research Forum



Learning outcomes:

- Introduction to getting started with your first technical paper.
- Recognize, explain, and use the formal elements of technical paper.
- Understand the ethical, international, social, and professional constraints of audience, style, and content for technical writing.

Important Dates:

- **Date:** 4th March, 2023
- **Time:** 11:00 AM – 01:00 PM
- **Venue:** R. No. B42 (4th floor)

Mrs. Neeta Chavan
Co-convenor

Mrs. Manisha Chattopadhyay
Dy. Head, Dept of EXTC

Dr. (Mrs.) M. Vijayalakshmi
Vice- Principal, VESIT

Dr.(Mrs.) Ranjan Bala Jain
Convenor

Dr. Chandan Singh Rawat
Head, Dept of EXTC

Shri. B. L. Boolani
Trustee, VESIT

Dr. (Mrs.) J. M. Nair
Principal, VESIT